



PATENT
Docket No.: ACT-367

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2811

Examiner: Not yet assigned

Serial No. 10/619,172

Filed: July 11, 2003

In re Application of: McCollum et al.

For: LOW-CAPACITANCE INPUT/OUTPUT AND ELECTROSTATIC
DISCHARGE CIRCUIT FOR PROTECTING AN INTEGRATED
CIRCUIT FROM ELECTROSTATIC DISCHARGE

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail, in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 12/10/03, Signed Stephanie Davis
Stephanie Davis

TRANSMITTAL OF FORMAL DRAWINGS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

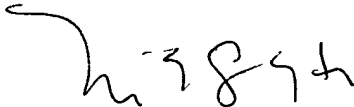
Dear Sir:

With respect to the above-identified patent application, enclosed herewith for filing are 7 (seven) sheets of Formal Drawings.

Respectfully submitted,
SIERRA PATENT GROUP, LTD.

Dated: December 10, 2003

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